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A scheduling algorithm for optimization and early planning in high-level synthesis

Seda Ogrenci Memik, Ryan Kastner, Elaheh Bozorgzadeh, Majid Sarrafzadeh January 2005 ACM Transactions on Design Automation of Electronic Systems (TODAES), Volume 10 Issue 1

Publisher: ACM Press

Full text available: pdf(235.21 KB) Additional Information: full citation, abstract, references, index terms

Complexities of applications implemented on embedded and programmable systems grow with the advances in capacities and capabilities of these systems. Mapping applications onto them manually is becoming a very tedious task. This draws attention to using highlevel synthesis within design flows. Meanwhile, it is essential to provide a flexible formulation of optimization objectives as well as to perform efficient planning for various design objectives early on in the design flow. In this work, we ...

Keywords: Scheduling, bipartite matching, data flow graph, high-level synthesis

2 Are wait-free algorithms fast?

Hagit Attiya, Nancy Lynch, Nir Shavit

July 1994 Journal of the ACM (JACM), Volume 41 Issue 4

Publisher: ACM Press

Full text available: pdf(2.58 MB)

Additional Information: full citation, abstract, references, citings, index terms, review

The time complexity of wait-free algorithms in "normal" executions, where no failures occur and processes operate at approximately the same speed, is considered. A lower bound of log n on the time complexity of any wait-free algorithm that achieves approximate agreement among n processes is proved. In contrast, there exists a nonwait-free algorithm that solves this problem in constant time. This implies an &OHgr; (log n< ...

Keywords: approximate agreement, fault-tolerance, wait-free

3 Automatic data layout for distributed-memory machines

Ken Kennedy, Ulrich Kremer

July 1998 ACM Transactions on Programming Languages and Systems (TOPLAS),

Volume 20 Issue 4 Publisher: ACM Press

Full text available: pdf(633.20 KB)

Additional Information: full citation, abstract, references, citings, index

terms, review

The goal of languages like Fortran D or High Performance Fortran (HPF) is to provide a simple yet efficient machine-independent parallel programming model. After the algorithm selection, the data layout choice is the key intellectual challenge in writing an efficient program in such languages. The performance of a data layout depends on the target compilation system, the target machine, the problem size, and the number of available processors. This makes the choice of a good layout extremel ...

Keywords: high performance Fortran

Fast deterministic consensus in a noisy environment

James Aspnes

July 2000 Proceedings of the nineteenth annual ACM symposium on Principles of distributed computing

Publisher: ACM Press

Full text available: pdf(1.10 MB)

Additional Information: full citation, abstract, references, citings, index

It is well known that the consensus problem cannot be solved deterministically in an asynchronous environment, but that randomized solutions are possible. We propose a new model, called noisy scheduling, in which an adversarial schedule is perturbed randomly, and show that in this model randomness in the environment can substitute for randomness in the algorithm. In particular, we show that a simplified, deterministic version of Chandra's wait-free shared-m ...

Compiler scheduling: Convergent scheduling

Walter Lee, Diego Puppin, Shane Swenson, Saman Amarasinghe

November 2002 Proceedings of the 35th annual ACM/IEEE international symposium on Microarchitecture

Publisher: IEEE Computer Society Press

Full text available: Additional Information: full citation, abstract, references, citings, index Publisher Site

Convergent scheduling is a general framework for cluster assignment and instruction scheduling on spatial architectures. A convergent scheduler is composed of independent passes, each implementing a heuristic that addresses a particular problem or constraint. The passes share a simple, common interface that provides spatial and temporal preference for each instruction. Preferences are not absolute; instead, the interface allows a pass to express the confidence of its preferences, as well as pref ...

Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren

November 1997 Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research

Publisher: IBM Press

Additional Information: full citation, abstract, references, index terms Full text available: pdf(4.21 MB)

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

7 Communication scheduling

Peter Mattson, William J. Dally, Scott Rixner, Ujval J. Kapasi, John D. Owens November 2000 ACM SIGARCH Computer Architecture News, ACM SIGOPS Operating Systems Review, Proceedings of the ninth international conference on Architectural support for programming languages and operating systems ASPLOS-IX, Volume 28, 34 Issue 5, 5

Publisher: ACM Press

Full text available: pdf(149.45 KB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms

The high arithmetic rates of media processing applications require architectures with tens to hundreds of functional units, multiple register files, and explicit interconnect between functional units and register files. Communication scheduling enables scheduling to these emerging architectures, including those that use shared buses and register file ports. Scheduling to these shared interconnect architectures is difficult because it requires simultaneously allocating functional units to operati ...

8 Communication scheduling

Peter Mattson, William J. Dally, Scott Rixner, Ujval J. Kapasi, John D. Owens November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Publisher: ACM Press

Full text available: pdf(1.12 MB) Additional Information: full citation, abstract, references, index terms

The high arithmetic rates of media processing applications require architectures with tens to hundreds of functional units, multiple register files, and explicit interconnect between functional units and register files. Communication scheduling enables scheduling to these emerging architectures, including those that use shared buses and register file ports. Scheduling to these shared interconnect architectures is difficult because it requires simultaneously allocating functional units to operati ...

9 Special issue: Al in engineering

D. Sriram, R. Joobbani

April 1985 ACM SIGART Bulletin, Issue 92

Publisher: ACM Press

Full text available: pdf(8.79 MB) Additional Information: full citation, abstract

The papers in this special issue were compiled from responses to the announcement in the July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers received from over six countries. About half the papers were received over the computer network.

10 <u>Two-dimensional round-robin schedulers for packet switches with multiple input queues</u>

Richard O. LaMaire, Dimitrios N. Serpanos

October 1994 IEEE/ACM Transactions on Networking (TON), Volume 2 Issue 5

Publisher: IEEE Press

Full text available: pdf(1.24 MB)

Additional Information: full citation, references, citings, index terms,

review

11 Advances in embedded software scheduling techniques: A low power scheduler

using game theory

N. Ranganathan, Ashok K. Murugavel

October 2003 Proceedings of the 1st IEEE/ACM/IFIP international conference on Hardware/software codesign and system synthesis

Publisher: ACM Press

Full text available: pdf(180.91 KB) Additional Information: full citation, abstract, references, index terms

In this paper, we describe a new methodology based on game theory for minimizing the average power of a circuit during scheduling in behavioral synthesis. The problem of scheduling in data-path synthesis is formulated as an auction based non-cooperative finite game, for which solutions are developed based on the Nash equilibrium function. Each operation in the data-path is modeled as a player bidding for executing an operation in the given control cycle, with the estimated power consumption as t ...

Keywords: auction theory, game theory, high-level synthesis, low power design

12 Technical reports

SIGACT News Staff

January 1980 ACM SIGACT News, Volume 12 Issue 1

Publisher: ACM Press

Full text available: pdf(5.28 MB)

Additional Information: full citation

13 Applications in logistics, transportation, and distribution: Simulation planning and rostering: runway schedule determination by simulation optimization

Thomas Curtis Holden, Frederick Wieland

December 2003 Proceedings of the 35th conference on Winter simulation: driving innovation

Publisher: Winter Simulation Conference

Full text available: pdf(446.86 KB) Additional Information: full citation, abstract, references

Many airport runway expansion projects are restricted by space limitations imposed by development in the vicinity of the airport. This often causes planners to choose configurations for new runways that limit the use of these runways in time and/or space. Studies that model airports with new runways that are not yet operational need to develop plausible operational models for these new runways since historical data is not available. We look at a runway schedule problem encountered during the ...

14 <u>University of Maryland student scheduling algorithm</u>

James Stewart, Robert L. Clark

January 1968 Proceedings of the 1968 23rd ACM national conference

Publisher: ACM Press

Full text available: 🔂 pdf(750.84 KB) Additional Information: full citation, abstract, references, index terms

This report describes the basic algorithm by which students are assigned to classes, the various criteria which directed the design of that algorithm, and the consequences of some of its features. The University of Maryland Student Scheduling Program consists of two separate phases: a catalogue builder and a student scheduler. These programs operate on the IBM 7094 and are described in the last section of this report. Since the subject of this report is the basic scheduling algor ...

15 A SMART scheduler for multimedia applications

Jason Nieh, Monica S. Lam

May 2003 ACM Transactions on Computer Systems (TOCS), Volume 21 Issue 2

Publisher: ACM Press

Full text available: pdf(570.87 KB) Additional Information: full citation, abstract, references, index terms

Real-time applications such as multimedia audio and video are increasingly populating the workstation desktop. To support the execution of these applications in conjunction with traditional non-real-time applications, we have created SMART, a Scheduler for Multimedia And Real-Time applications. SMART supports applications with time constraints, and provides dynamic feedback to applications to allow them to adapt to the current load. In addition, the support for real-time applications is integrat ...

Keywords: Scheduling, multimedia, proportional sharing, real-time

16 A formal study of distributed meeting scheduling: preliminary results

Sandip Sen, Edmund H. Durfee

October 1991 ACM SIGOIS Bulletin, Conference proceedings on Organizational computing systems COCS '91, Volume 12 Issue 2-3

Publisher: ACM Press

Full text available:

Additional Information:

full citation, references, citings, index terms

17 Space-time scheduling of instruction-level parallelism on a raw machine

Walter Lee, Rajeev Barua, Matthew Frank, Devabhaktuni Srikrishna, Jonathan Babb, Vivek Sarkar, Saman Amarasinghe

October 1998 ACM SIGPLAN Notices, ACM SIGOPS Operating Systems Review, Proceedings of the eighth international conference on Architectural support for programming languages and operating systems ASPLOS-VIII, Volume 33, 32 Issue 11, 5

Publisher: ACM Press-

Full text available: pdf(1.79 MB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms

Increasing demand for both greater parallelism and faster clocks dictate that future generation architectures will need to decentralize their resources and eliminate primitives that require single cycle global communication. A Raw microprocessor distributes all of its resources, including instruction streams, register files, memory ports, and ALUs, over a pipelined two-dimensional mesh interconnect, and exposes them fully to the compiler. Because communication in Raw machines is distributed, com ...

18 A continuum of disk scheduling algorithms

Robert Geist, Stephen Daniel

January 1987 ACM Transactions on Computer Systems (TOCS), Volume 5 Issue 1

Publisher: ACM Press

Full text available: pdf(866.07 KB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> <u>terms</u>, <u>review</u>

A continuum of disk scheduling algorithms, V(R), having endpoints V(0) = SSTF and V(1) = SCAN, is defined. V(R) maintains a current SCAN direction (in or out) and services next the request with the smallest effective distance. The effective distance of a request that lies in the current direction is its physical distance (in cylinders) from the read/write head. The effective distance of a request in the opposite direction is its physical di ...

19 <u>Distributed visualization of graph algorithms</u>

Alexander A. Sherstov

January 2003 ACM SIGCSE Bulletin , Proceedings of the 34th SIGCSE technical symposium on Computer science education SIGCSE '03, Volume 35 Issue 1 Publisher: ACM Press

Publisher. Acid Fless

Full text available: pdf(218.85 KB) Additional Information: full citation, abstract, references, index terms

DisViz is a visualization tool designed to assist students in learning graph algorithms, an important topic in the undergraduate curriculum. DisViz is intended for collaborative use by a group of students over a classroom network. This visualization system views network hosts as graph nodes and the socket connections among them, as graph edges. In typical usage, every student runs a copy of DisViz on his/her local machine. These applications detect each other's presence on the network and coordi ...

Keywords: computer science education, distributed computing, graph algorithms, simulation and modeling, visualization

20 Research sessions: query processing II: Minimal probing: supporting expensive

predicates for top-k queries

Kevin Chen-Chuan Chang, Seung-won Hwang

June 2002 Proceedings of the 2002 ACM SIGMOD international conference on Management of data SIGMOD '02

Publisher: ACM Press

Full text available: pdf(1.53 MB)

Additional Information: full citation, abstract, references, citings, index

terms

This paper addresses the problem of evaluating ranked *top-k* queries with expensive predicates. As major DBMSs now all support expensive user-defined predicates for Boolean queries, we believe such support for ranked queries will be even more important: First ranked queries often need to model user-specific concepts of preference, relevance, or similarity, which call for dynamic user-defined functions. Second, middleware systems must incorporate external predicates for integrating autonomo ...

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Patent Assignment Abstract of Title

Total Assignments: 2

Application #: 09828463

Filing Dt: 04/06/2001

Patent #: NONE

Issue Dt:

PCT #: NONE

Publication #: US20020023274

Pub Dt: 02/21/2002

Inventor: Louis D. Giacalone, JR.

Title: Method and system for electronically distributing, displaying and controlling advertising and other

communicative media

Assignment: 1

Reel/Frame: 015865 / 0438 Received: 04/06/2005 Recorded: 04/06/2005 Mailed: 04/06/2005 Pages: 6

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: GIACALONE, JR., LOUIS D.

Exec Dt: 04/05/2005

Assignee: COOLSIGN MEDIA, INC.

270 EAST LANE

BURLINGAME, CALIFORNIA 94010

Correspondent: BRIAN R. COLEMAN

101 JEFFERSON DRIVE MENLO PARK, CA 94025

Assignment: 2

Reel/Frame: 015889 / 0055 Received: 04/11/2005 Recorded: 04/11/2005 Mailed: 04/12/2005 Pages: 5

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: COOLSIGN MEDIA, INC. Exec Dt: 04/06/2005

Assignee: CLARITY VISUAL SYSTEMS, INC.

27530 SW 95TH AVENUE

SUITE 3038

WILSONVILLE, OREGON 97070

Correspondent: BRIAN R. COLEMAN

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[PS] To appear in MICRO-35, November 2002, Istanbul, Turkey Convergent ...

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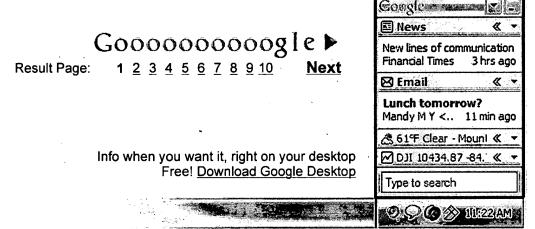
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Kam, A.C.; Kai-Yeung Siu;

Selected Areas in Communications, IEEE Journal on

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Volume 18, Issue 10, Oct. 2000 Page(s):2029 - 2040

Digital Object Identifier 10.1109/49.887922

AbstractPlus | References | Full Text: PDF(216 KB) IEEE JNL

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2. Optimal vegetation maintenance scheduling of overhead electric power distribution systems

Kuntz, P.A.; Christie, R.D.; Venkata, S.S.;

Power Delivery, IEEE Transactions on

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3. Adaptively scheduling parallel loops in distributed shared-memory systems

Yong Yan; Canming Jin; Xiaodong Zhang;

Parallel and Distributed Systems, IEEE Transactions on

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Digital Object Identifier 10.1109/71.569656

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Muresan, V.; Wang, X.; Vladutiu, M.;

Rapid System Prototyping, 12th International Workshop on, 2001.

25-27 June 2001 Page(s):162 - 167

Digital Object Identifier 10.1109/IWRSP.2001.933855

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5. An adaptive loop scheduling algorithm on shared-memory systems

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Parallel and Distributed Processing, 1996. Eighth IEEE Symposium on

23-26 Oct. 1996 Page(s):250 - 257

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6. Real-time scheduling of multiple segment tasks

Ho, K.; Rice, J.H.; Srivastava, J.;

Computer Software and Applications Conference, 1990. COMPSAC 90. Proceedings.,

Fourteenth Annual International

31 Oct.-2 Nov. 1990 Page(s):680 - 686

Digital Object Identifier 10.1109/CMPSAC.1990.139459

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A combined approach of scheduling and power distribution strategies for MIMO mobile communications system

Zhao Xinsheng; Ju Tao; Li Hui; Xin Chang; Schulz, E.;

Personal, Indoor and Mobile Radio Communications, 2004. PIMRC 2004. 15th IEEE

International Symposium on

Volume 2, 5-8 Sept. 2004 Page(s):1380 - 1383 Vol.2

AbstractPlus | Full Text: PDF(351 KB) | IEEE CNF

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8. A new wireless packet scheduling algorithm based on the CDF of user transmission rates

Daeyoung Park; Hanbyul Seo; Hojoong Kwon; Byeong Gi Lee;

Global Telecommunications Conference, 2003. GLOBECOM '03. IEEE

Volume 1, 1-5 Dec. 2003 Page(s):528 - 532 Vol.1

Digital Object Identifier 10.1109/GLOCOM.2003.1258293

AbstractPlus | Full Text: PDF(287 KB) | IEEE CNF

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Daeyoung Park; Seo, H.; Hojoong Kwon; Byeong Gi Lee;

Communications, IEEE Transactions on

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Digital Object Identifier 10.1109/TCOMM.2005.858675

AbstractPlus | Full Text: PDF(520 KB) | IEEE JNL

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Cohen, R.; Katzir, L.; Raz, D.;

INFOCOM 2002. Twenty-First Annual Joint Conference of the IEEE Computer and

Communications Societies. Proceedings. IEEE

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Digital Object Identifier 10.1109/INFCOM.2002.1019341

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Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium on

Volume 5, 6-9 May 2001 Page(s):255 - 258 vol. 5

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Murescan, V.; Xiaojun Wang; Vladutiu, M.;

Test Symposium, 2000. (ATS 2000). Proceedings of the Ninth Asian

4-6 Dec. 2000 Page(s):465 - 470

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13. Sensor schedùling algorithms requiring limited computation [vehicle sonar range-finder

example]

Gupta, V.; Chung, T.; Hassibi, B.; Murray, R.M.;

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Volume 3, 17-21 May 2004 Page(s):iii - 825-8 vol.3

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14. VirtualLength: a new packet scheduling algorithm for proportional delay differentiation

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15. An advanced content delivery scheduling method for block multicast transfer

Zheng Yu Xie; Uno, S.; Tode, H.; Murakami, K.;

Local Computer Networks, 2003. LCN '03. Proceedings. 28th Annual IEEE International

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Sapountzis, G.; Katevenis, M.;

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Elliott, R.C.; Krzymien, W.A.;

Vehicular Technology Conference, 2002. Proceedings. VTC 2002-Fall. 2002 IEEE 56th

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18. R×W: a scheduling approach for large-scale on-demand data broadcast

Aksoy, D.; Franklin, M.;

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Digital Object Identifier 10.1109/90.811450

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Ajmone Marsan, M.; Bianco, A.; Giaccone, P.; Leonardi, E.; Neri, F.;

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Volume 10, Issue 5, Oct. 2002 Page(s):666 - 678

Digital Object Identifier 10.1109/TNET.2002.803939

AbstractPlus | References | Full Text: PDF(429 KB) | IEEE JNL

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22. Wireless access to the World Wide Web in an integrated CDMA system

Comaniciu, C.; Mandayam, N.B.; Famolari, D.; Agrawal, P.;

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Volume 2, Issue 3, May 2003 Page(s):472 - 483

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L9	23	I7 and trigger adj event	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 12:43
L10	34	17 and frequency same algorithm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 12:43
L11	30	I7 and filter\$3 same algorithm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 12:44
L12	72	I7 and weight\$5 same algorithm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 13:29
L13	1	("6,446,045").PN.	US-PGPUB; USPAT	OR	OFF	2006/03/07 13:29
L14	0	(2004/0064497).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/03/07 13:29
L15	1	("20040064497").PN.	US-PGPUB; USPAT	OR	OFF	2006/03/07 16:50
L16	. 0	schedule near5 assigned near2 weight same (advertis\$5 or advertisement)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR /	ON	2006/03/07 16:52
L17	2	schedule near5 assigned near2 weight and (advertis\$5 or advertisement)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2006/03/07 16:52
L18	7	schedule near5 weight same (advertis\$5 or advertisement)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 16:54
L19	212	relativ\$4 near5 weight near10 frequency	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 16:54

L20	10	relativ\$4 near5 weight near10 frequency same schedul\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 16:57
L21	1102	advertisement near8 schedul\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT;	OR	ON	2006/03/07 16:58
L22	26	advertisement near8 schedul\$5 near5 assign\$5	IBM_TDB US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 16:58
L23	21	I22 and weight\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 16:58
L24	19	(US-20030080999-\$ or US-20040064497-\$ or US-20010052000-\$ or US-20020023274-\$ or US-20030145323-\$ or US-20010020236-\$).did. or (US-6195694-\$ or US-6494363-\$ or US-6311165-\$ or US-6494363-\$ or US-6286029-\$ or US-6477707-\$ or US-5850442-\$ or US-6519693-\$ or US-6714975-\$ or US-6463585-\$ or US-6286005-\$ or US-6009409-\$ or US-5848397-\$).did.	US-PGPUB; USPAT	OR	ON	2006/03/07 17:31
L25		I24 and recur\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 18:19
L26	19	recur\$5 near5 playback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 17:36
L27	1	I26 and advertisement	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 17:31

		LASI Searc	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			
L28	1	l26 and advertise	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 17:32
L29	2	I26 and recurring near5 period	US-PGPUB; USPAT;	OR	ON	2006/03/07 17:32
		•.	EPO; JPO; DERWENT; IBM_TDB			
L30	1	("20010020236").PN.	US-PGPUB; USPAT	OR	OFF	2006/03/07 17:36
L31	0	I24 and ondemand	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 18:22
L32	1	ondemand near5 advertisement	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 18:22
. L33	486	demand near5 advertisement	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 18:22
L34	8	124 and demand	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/07 18:22
L35	0	124 and on adj demand	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/03/07 18:22
S1	303	(ATM or Kiosk) and gateway and (content same schedul\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/07 12:40
S2	. 9	(ATM or Kiosk) and gateway and (content same schedul\$3 same algorithm)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:11
S3	1	(Kiosk) and gateway and (content same schedul\$3 same algorithm)	US-PGPUB; USPAT; EPO; JPO	OR	ON .	2004/06/27 15:12
S4	0	(advertis same bill\$3) same schedul\$3 and (content same schedul\$3 same algorithm)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:13

				, <u> </u>		
S5	0	(advertis same bill\$3) and (schedul\$3 same algorithm) and (Kiosk or ATM)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:13
S6	33	(advertis\$5 same bill\$3) and (schedul\$3 same algorithm) and (Kiosk or ATM)	US-PGPUB; USPAT; EPO; JPO	OR	ON.	2004/06/27 15:14
.S7 :	0 .	(advertis\$5_same_bill\$3)_and (schedul\$3 same algorithm) and (Kiosk or ATM) and (dynamic\$5 near8 advertis\$5)	US-PGPUB; USPAT; EPO; JPO	_OR	-ON	2004/06/27 15:14
S8	3	(advertis\$5 same bill\$3) and (schedul\$3 same algorithm) and (Kiosk or ATM) and (dynamic\$5 same advertis\$5)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:15
S9	. 0	(advertis\$5 same bill\$3) and (schedul\$3 same algorithm) and (Kiosk) and (dynamic\$5 same advertis\$5)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:15
S10	48	(advertis\$5 and bill\$3) and (schedul\$3 same algorithm) and (Kiosk) and (dynamic\$5)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:18
S11	5.	((schedul\$5) near8 advertis\$5 same Kiosk) and (dynamic\$5)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:21
S12	8	((schedul\$5) same advertis\$5 same Kiosk) and (dynamic\$5)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:21
S13	3	(((schedul\$5) same advertis\$5 same Kiosk) and (dynamic\$5)) not (((schedul\$5) near8 advertis\$5 same Kiosk) and (dynamic\$5))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:23
S14	5	(("6195694") or ("5850442") or ("6659342") or ("6494363") or ("6311165")).PN.	USPAT; USOCR	OR	OFF	2004/06/27 15:29
S15	213	schedul\$3 near8 distribut\$5 near8 content	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:30
S16	9	(schedul\$3 near8 distribut\$5 near8 content) and (scheduling near5 algorithm)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:34
S17	10	advertis\$5 same billing same kiosk	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/06/27 15:35
S18	183	schedul\$5 near5 distribut\$5 and play\$5 same advertis\$5	US-PGPUB; USPAT; EPO; JPO	OR .	ON	2005/05/29 13:50

		<u> </u>				
S19	73	schedul\$5 near5 distribut\$5 and play\$5 near5 advertis\$5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/29 13:50
S20	59	S19 and broadcast\$5	US-PGPUB; USPAT; EPO; JPO	OR ·	ON	2005/05/29 13:51
S21	52_	S20 and server	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/29 13:51
S22	50	S21 and display\$5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/29 13:51
S23	1	("6446045").PN.	USPAT	OR	OFF	2005/05/29 14:20
S24	4	algorithm near5 schedul\$5 near5 play\$3 same (frequency or interval or (tim\$3 near play\$5) or (trigger near3 event) or filter\$5)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/05/29 15:05
S25	0	S24 and (receiver near5 server)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/05/29 15:06
S26	0	S24 and (receiver same server)	US-PGPUB; USPAT; EPO; JPO	OR ·	OFF	2005/05/29 15:07
S27	0	intermediate near2 server same loss near5 coupling	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/29 15:08
S28	0	intermediate near2 server same loss near5 coupl\$5	US-PGPUB; USPAT; EPO; JPO	OR	ON .	2005/05/29 15:08
S29	10	intermediate near2 server same loss	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/29 15:12
S30 ⁻	3	S29 and broadcast\$5	US-PGPUB; USPAT; EPO; JPO	OR	ON .	2005/05/29 15:12
S31	11	intermediate near3 server same loss	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/29 15:12
S32	4	S31 and broadcast\$5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/29 15:12